

Amendments to the claims

Claim 1 (currently amended): A microprocessor comprising:

an identifier that identifies the microprocessor; and

embedded instructions that comprise microcode for comparing a hash value, derived from the identifier and a key, to an expected hash value.

Claim 2 (currently amended): The microprocessor of claim 1 further comprising embedded instructions that comprise microcode for producing a hash value that is a function of the identifier and a key.

Claim 3 (original): The microprocessor of claim 2 wherein the identifier comprises a processor number.

Claim 4 (canceled)

Claim 5 (currently amended): The microprocessor of claim 1 [[4]] wherein the key corresponds to an address for a web site.

Claim 6 (original): The microprocessor of claim 5 wherein the expected hash value is derived from a key that corresponds to an address for a web site and a processor number.

Claims 7-24 (canceled)